

(12) United States Patent

Mirkazemi-Moud et al.

(54) SECURE POINT OF SALE TERMINAL

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USPC 713/194; 726/34; 340/539.1, 539.11, 340/541, 568.2, 568.8, 571, 572.8; 341/20;

345/168; 361/170, 679.4; 235/128

See application file for complete search history.

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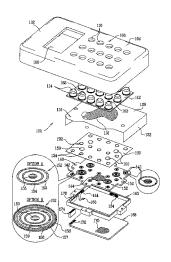
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ABSTRACT

A data entry device including a housing formed of at least two portions, data entry circuitry located within the housing, at least one case-open switch assembly operative to sense when the housing is opened and tamper indication circuitry operative to receive an input from the at least one case-open switch assembly and to provide an output indication of possible tampering with the data entry circuitry located within the housing, the at least one case-open switch assembly including an arrangement of electrical contacts including at least first, second and third contacts and a displaceable conductive element, the tamper indication circuitry and the third contact together being operative such that when the third contact is short circuited to at least one of the first contact, the second contact and another contact, an output indication of possible tampering is provided.

15 Claims, 20 Drawing Sheets

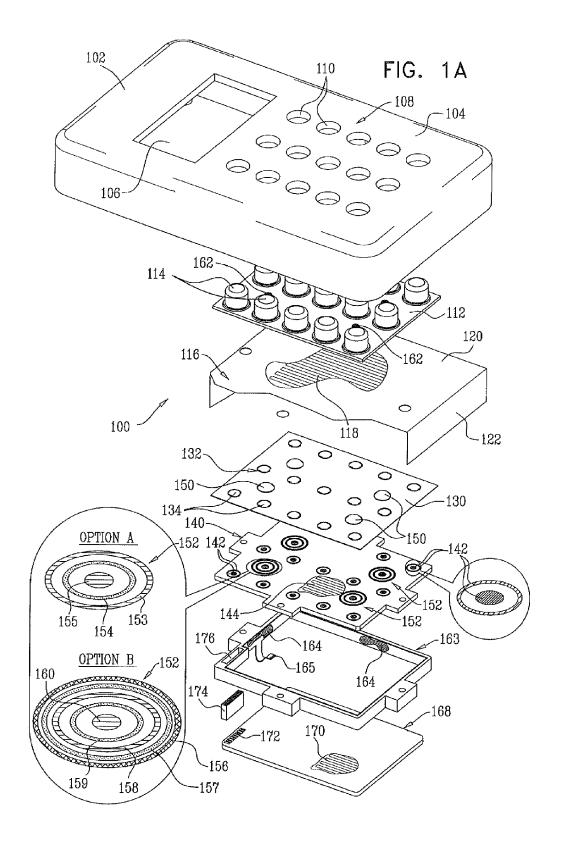


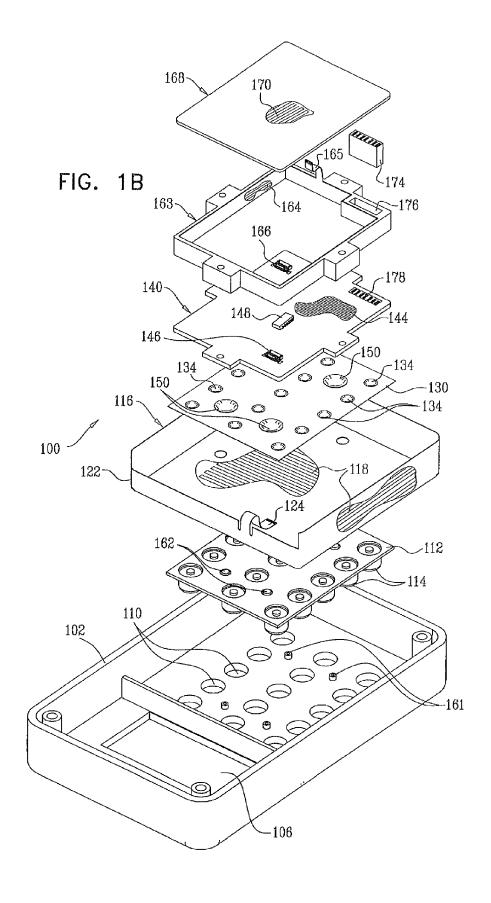
2001/0032211 A1 10/2001 Kuzumaki Related U.S. Application Data 2004/0031673 A1 2/2004 Levy now Pat. No. 8,595,514, which is a continuation-in-2004/0118670 A1 6/2004 Park et al. 2005/0138433 A1 6/2005 part of application No. 12/355,857, filed on Jan. 19, Linetsky 2005/0184870 A1 8/2005 Galperin et al. 2009, now Pat. No. 9,013,336. 2007/0016963 A1 1/2007 Robinson 2007/0040674 A1 (60) Provisional application No. 61/011,993, filed on Jan. 2/2007 Hsu 2007/0196156 A1 8/2007 Lewis 22, 2008. 2007/0204173 A1 8/2007 Kuhn 2008/0180245 A1 7/2008 Hsu et al. (51) **Int. Cl.** 2008/0278353 A1 11/2008 Smith et al. G06F 1/16 (2006.01)2009/0058628 A1 3/2009 Kirmayer G06F 3/02 (2006.01)2011/0063109 A1 3/2011 Ostermoller 2011/0215938 A1 9/2011 Neo et al. G06F 21/83 (2013.01)2011/0248860 A1 10/2011 Avital et al. G06F 21/86 (2013.01)11/2011 2/2012 2011/0279279 A1 Mirkazemi-Moud et al. H04M 1/23 (2006.01)2012/0025983 A1 Ben-Zion et al. 2012/0106113 A1 5/2012 (52) **U.S. Cl.** Kirmayer 2012/0180140 A1 7/2012 Barrowman et al. CPC G06F 1/1684 (2013.01); G06F 3/0219 2013/0067534 A1 3/2013 Soffer (2013.01); G06F 21/83 (2013.01); G06F 21/86 2014/0091930 A1 4/2014 Mirkazemi-Moud et at (2013.01); H04M 1/23 (2013.01) FOREIGN PATENT DOCUMENTS (56)References Cited EP 0375545 A1 6/1990 U.S. PATENT DOCUMENTS EP 1432031 A1 6/2004 EP1676182 A1 7/2006 3/1976 Yoder 3,941,964 A 2806507 A1 9/2001 FR 4,486,637 A 12/1984 Chu FR 2911000 A1 7/2008 4,527,030 A 7/1985 Oelsch 892198 A1 3/1962 GB 4,593,384 A 6/1986 Kleijne 1369739 GB 10/1974 4,749,368 A 6/1988 Mouissie 2178235 2/1987 GB 4,807,284 A 2/1989 Kleijne GB 2372363 A 8/2002 4,847,595 A 7/1989 Okamoto 2411756 A GB 9/2005 5,086,292 A 2/1992 Johnson et al. JР 2002-108711 A 4/2002 5.237.307 A 8/1993 Gritton JP 2003-100169 A 4/2003 5,239,664 A 8/1993 Verrier et al. WO 01/63994 A2 8/2001 5.353.350 A 10/1994 Unsworth et al. 03/019467 A1 WO 3/2003 5,506,566 A 4/1996 Oldfield et al. WO 2005/041002 A1 5/2005 5,559,311 A 9/1996 Gorbatoff 2005/086546 A2 9/2005 WO 5,586,042 A 12/1996 Pisau et al. 2009/091394 A1 WO 7/20095,675,319 A 10/1997 Rivenberg et al. 2010/082190 A1 WO 7/2010 5,809,143 A 9/1998 Hughes 5,861,662 A 1/1999 Candelore OTHER PUBLICATIONS 5,877,547 A 3/1999 Rhelimi 12/1999 5,998,858 A Little et al. Dave Van Ess; "Capacitive Touch Switches for Automotive Applica-6,288,640 B1 9/2001 Gagnon tions", Published in Automotive DesignLine http://www. 6,359,338 B1 3/2002 Takabayashi automotivedesignline.com/, Feb. 2006 7 pages. 6,396,400 B1 5/2002 Epstein, III et al. International Search Report and Written Opinion dated Apr. 30, 6,414,884 B1 7/2002 DeFelice et al. 6,438,825 B1 8/2002 2012; PCT/US12/20142 Kuhn 6,463,263 B1 10/2002 Feilner et al. U.S. Appl. No. 61/011,993, filed Jan. 22, 2008. 6,466,118 B1 10/2002 Van Zeeland et al. USPTO NFOA mailed May 28, 2004 in connection with U.S. Appl. 6,563,488 B1 5/2003 Rogers et al. No. 10/326,726. 6,571,948 B2 6/2003 Jones USPTO NFOA mailed Mar. 27, 2009 in connection with U.S. Appl. 6,633,241 B2 10/2003 Kaikuranta et al. No. 11/585,673 6,646,565 B1 11/2003 Fu et al. USPTO FOA mailed Dec. 14, 2009 in connection with U.S. Appl. No. Rogers et al. 6,669,100 B1 12/2003 6,830,182 B2 12/2004 Izuyama USPTO NFOA mailed Mar. 13, 2008 in connection with U.S. Appl. 6,853,093 B2 2/2005 Cohen et al. No. 11/636 369 6,874,092 B1 3/2005 Motoyama et al. 6,912,280 B2 6,917,299 B2 USPTO NFOA mailed May 13, 2010 in connection with U.S. Appl. 6/2005 Henry 7/2005 Fu et al. No. 11/845,435. USPTO NOA mailed Sep. 10, 2010 in connection with U.S. Appl. 6,921,988 B2 7/2005 Moree 6,936,777 B1 7,270,275 B1 8/2005 Kawakubo No. 11/845,435 9/2007 Moreland et al. USPTO NFOA dated Jul. 11, 2014 in connection with U.S. Appl. No. 7,283,066 B2 10/2007 Shipman 12/355,857 7,497,378 B2 3/2009 Aviv USPTO NOA mailed Sep. 13, 2013 in connection with U.S. Appl. 7,784,691 B2 8/2010 Mirkazemi-Moud et al. No. 12/666,054 7.835.521 B1 11/2010 Pinheiro USPTO NFOA dated Apr. 10, 2012 in connection with U.S. Appl. No. 7,843,339 B2 11/2010 Kirmayer 12/758,150. 7,898,413 B2 3/2011 Hsu et al. USPTO NFOA dated Nov. 21, 2013 in connection with U.S. Appl. 8,513,548 B2 8/2013 Rollet et al. No. 13/717,957. 8,595,514 B2 11/2013 Mirkazemi-Moud et al. 9,013,336 B2 4/2015 USPTO NOA mailed Feb. 24, 2014 in connection with U.S. Appl. No. Schulz et al

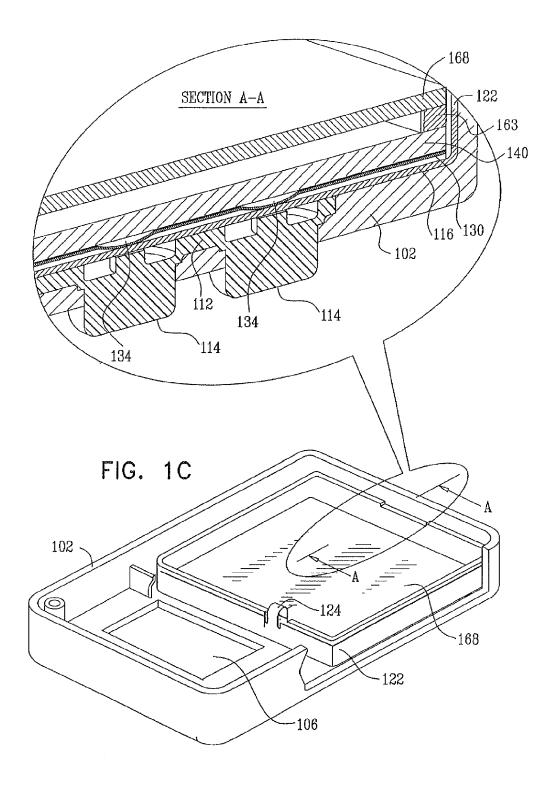
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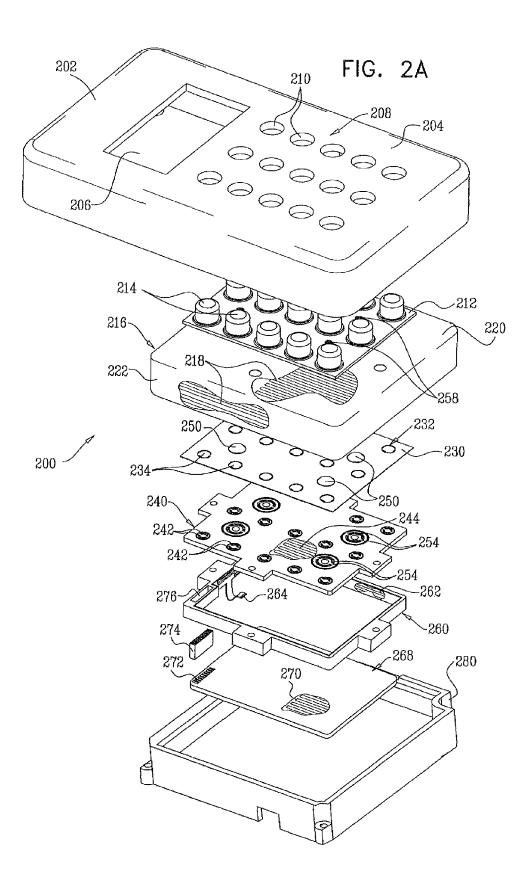
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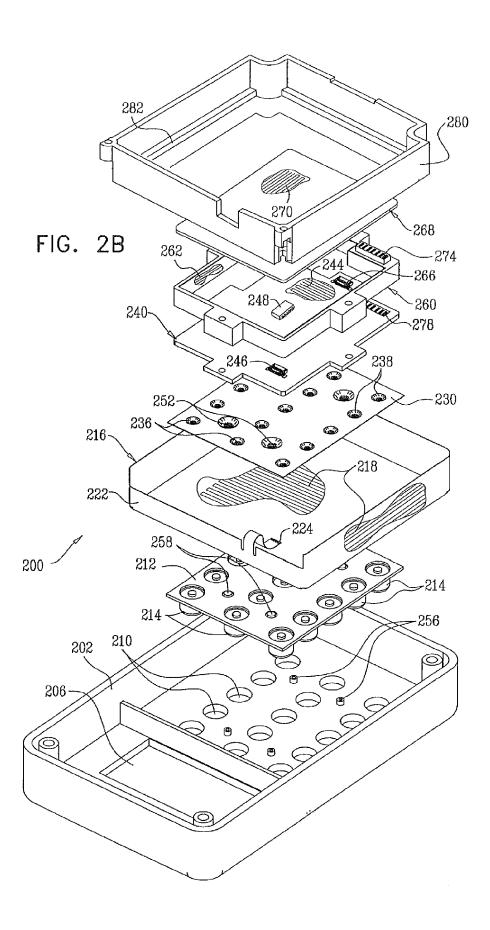
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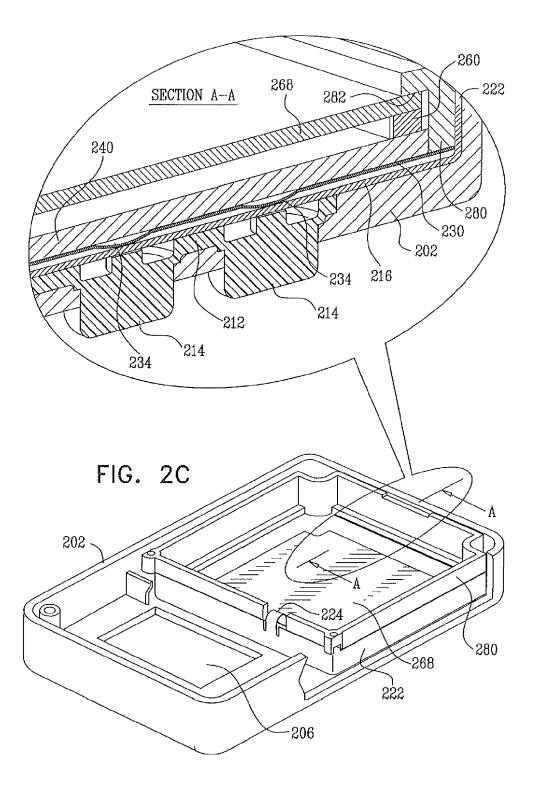












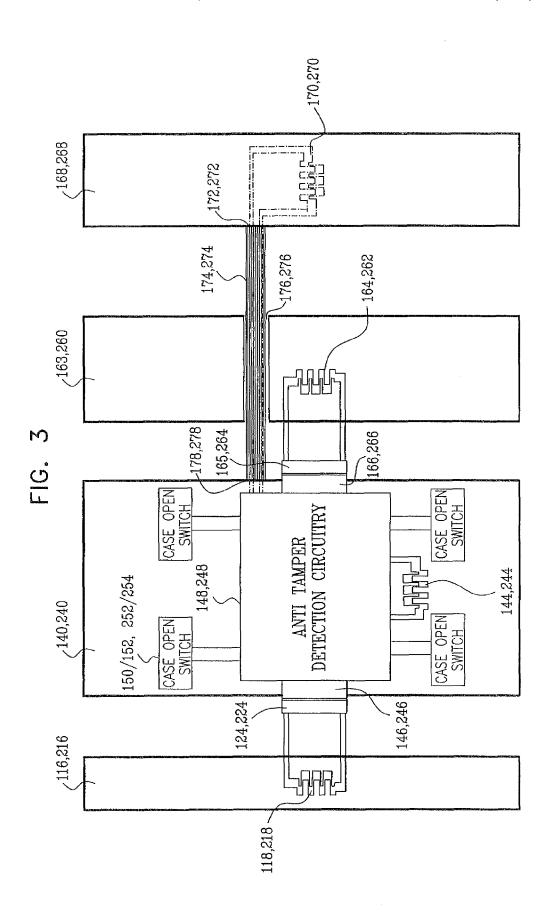
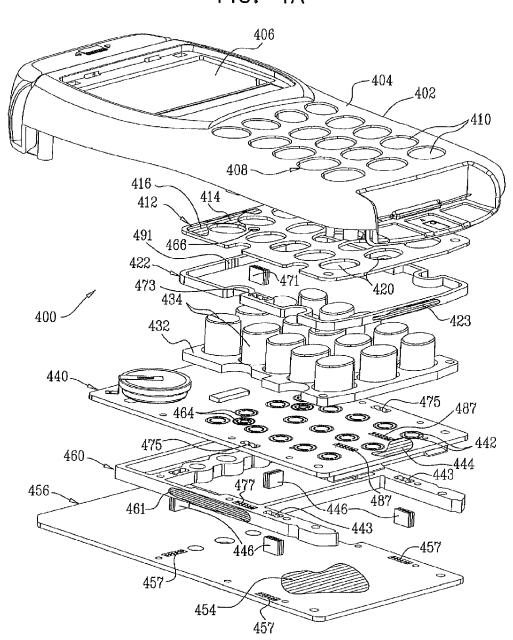
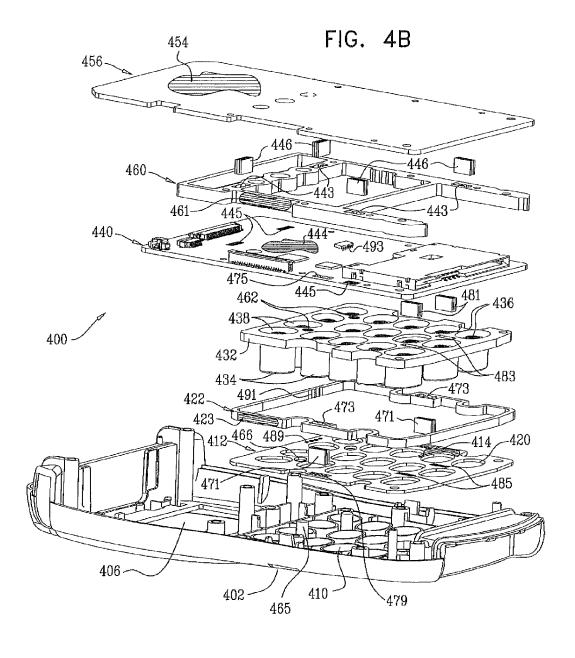
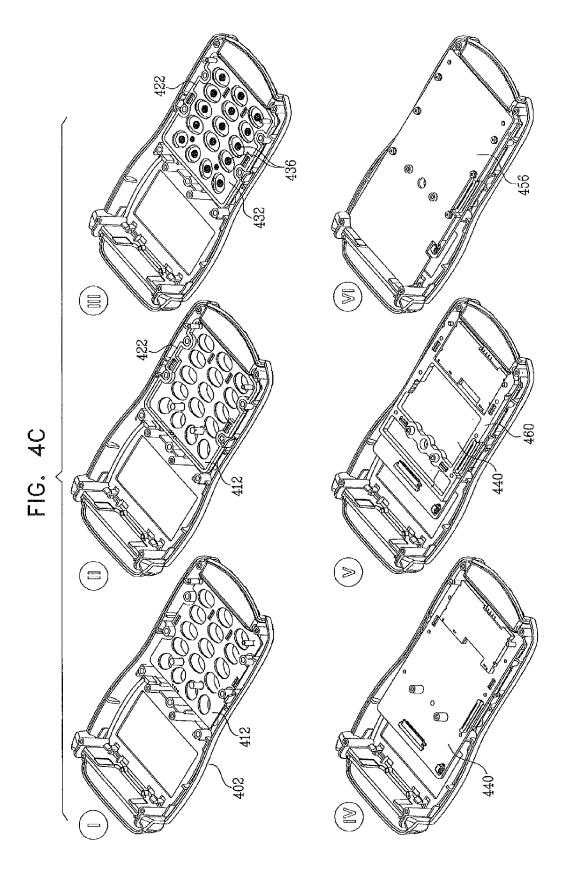


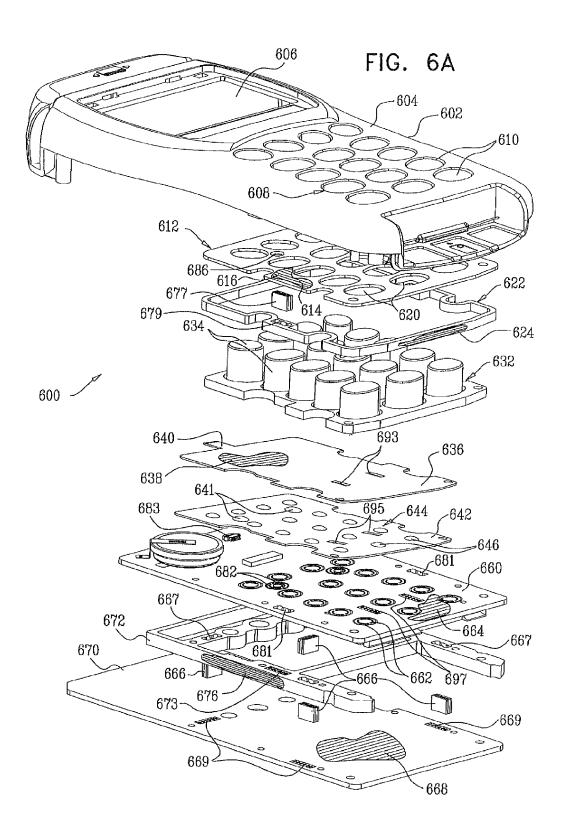
FIG. 4A

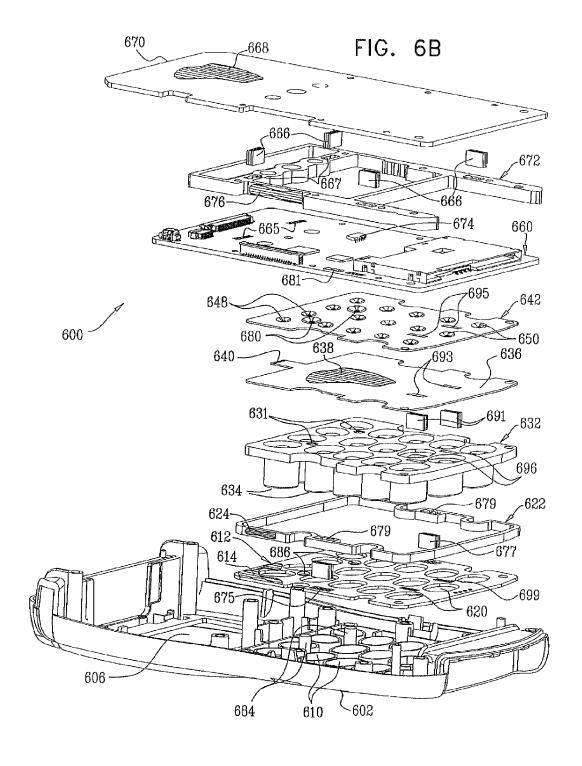




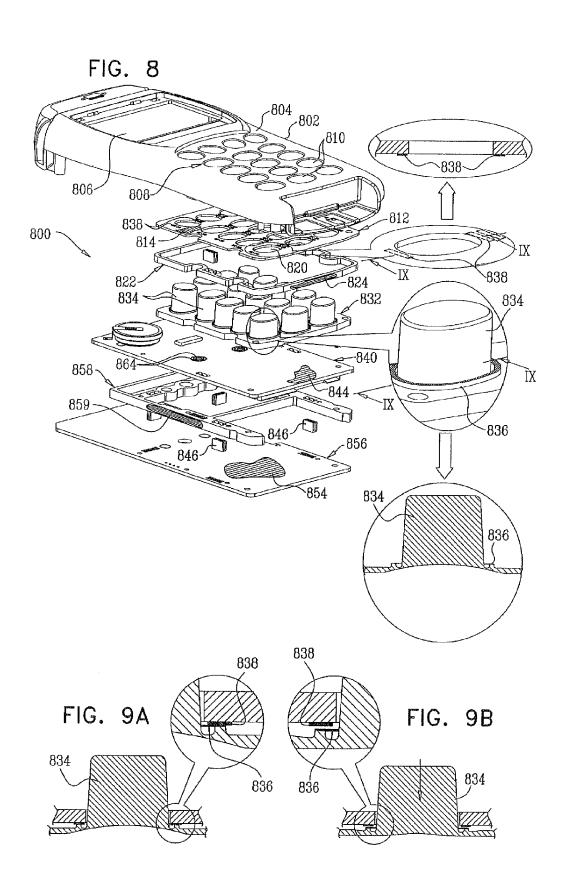


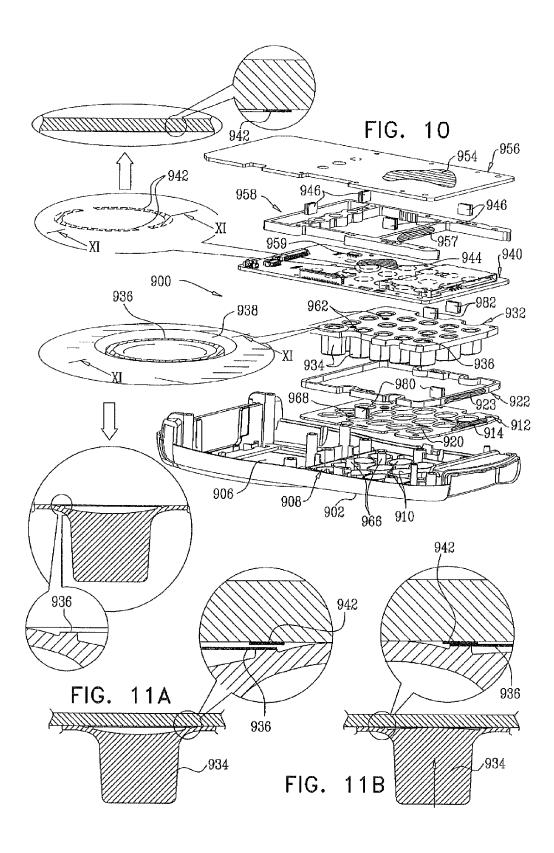
JAN. 454 457 457 446 CASE OPEN SWITCH 487~ 481 481 489

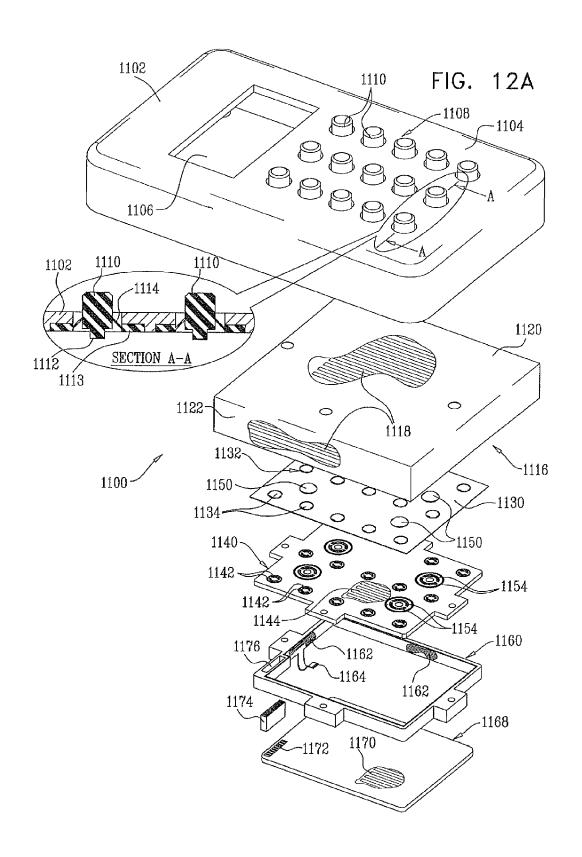




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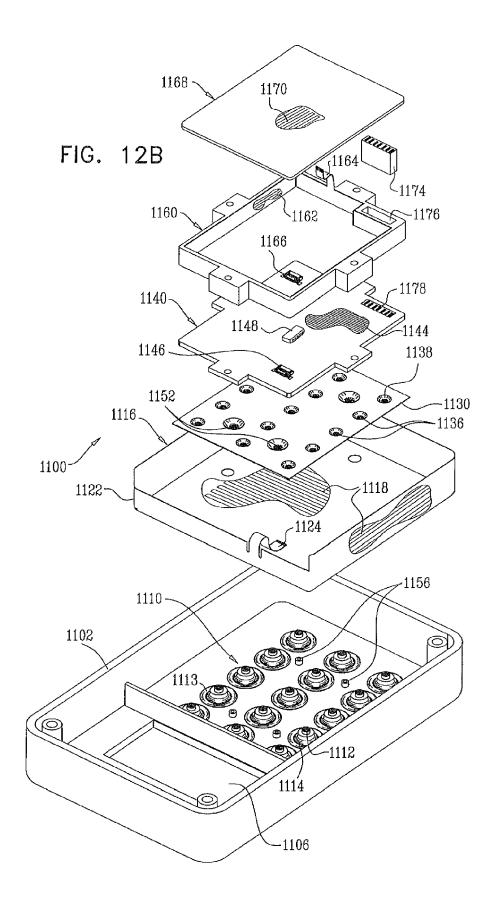


FIG. 13A

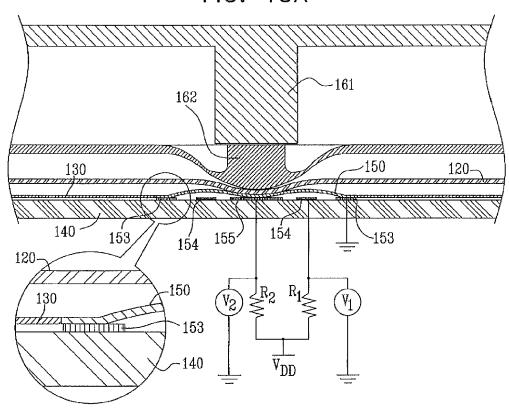


FIG. 13B

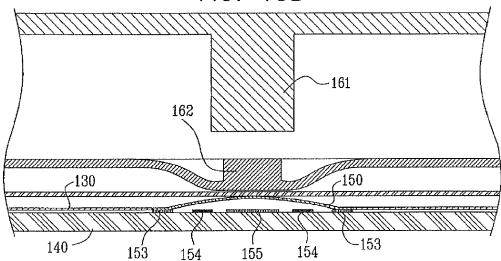


FIG. 14A

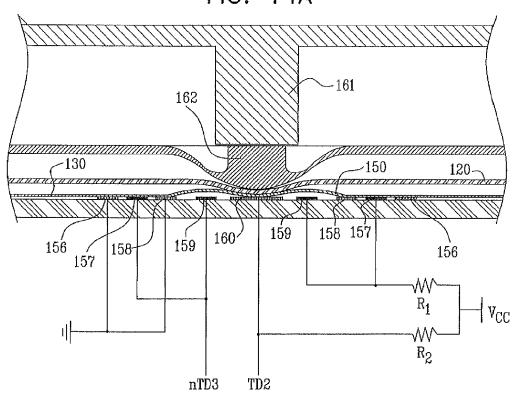
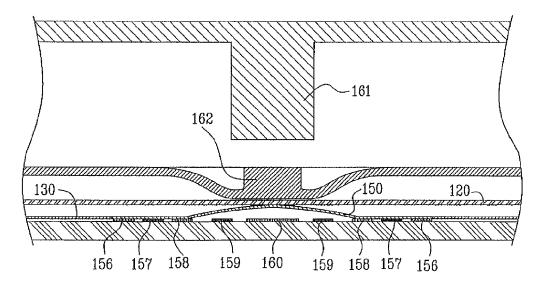


FIG. 14B



SECURE POINT OF SALE TERMINAL

REFERENCE TO RELATED APPLICATIONS

This application is a continuation of U.S. patent application Ser. No. 14/082,704, filed Nov. 18, 2013, titled SECURE POINT OF SALE TERMINAL, which is a continuation of U.S. patent application Ser. No. 12/666,054, filed Aug. 1, 2011, titled SECURE POINT OF SALE TERMINAL, now U.S. Pat. No. 8,595,514, which is a U.S. National Phase application of PCT Application No. PCT/IL2009/000724, filed Jul. 23, 2009, titled SECURE POINT OF SALE TERMINAL, which is continuation-in-part of U.S. patent application Ser. No. 12/355,857, filed Jan. 19, 2009, titled SECURED KEYPAD DEVICES, which claims priority of U.S. Provisional Patent Application Ser. No. 61/011,993, filed Jan. 22, 2008, titled SECURED KEYPAD DEVICES, the disclosures of which are incorporated by reference.

Reference is made to the following patent and patent applications, owned by assignee, the disclosures of which are hereby incorporated by reference, which are believed to relate to subject matter similar to the subject matter of the present application:

U.S. Pat. No. 6,853,093 and U.S. Published Patent Application Nos. 2008/0135617 and 2007/0152042.

FIELD OF THE INVENTION

The present invention relates generally to secure keypad ³⁰ devices and more particularly to data entry devices having anti-tamper functionality.

BACKGROUND OF THE INVENTION

The following U.S. patent Publications are believed to represent the current state of the art and are hereby incorporated by reference:

U.S. Published Patent Application No. 2008/0278353;U.S. Pat. Nos. 7,270,275; 6,646,565; 6,917,299 and 4,486, 40 637.

European Patent Nos.: 1421549 and 1676182; and Great Britain Patent Application No. GB8608277.

SUMMARY OF THE INVENTION

The present invention seeks to provide improved secure keypad devices.

There is thus provided in accordance with a preferred embodiment of the present invention a data entry device 50 including a housing formed of at least two portions, data entry circuitry located within the housing, at least one case-open switch assembly operative to sense when the housing is opened and tamper indication circuitry operative to receive an input from the at least one case-open switch assembly and to 55 provide an output indication of possible tampering with the data entry circuitry located within the housing, the at least one case-open switch assembly including an arrangement of electrical contacts including at least first, second and third contacts and a displaceable conductive element, which defines a 60 short circuit between the first and second contacts only when the housing is closed, the third contact being arranged between the first and second contacts and the displaceable conductive element so as to normally not be short circuited to the first and second contacts when the housing is closed, the 65 tamper indication circuitry and the third contact together being operative such that when the third contact is short

2

circuited to at least one of the first contact, the second contact and another contact, an output indication of possible tampering is provided.

In accordance with a preferred embodiment of the present invention, at least one of the first contact, the second contact and another contact is connected to the tamper indication circuitry. Preferably, the first, second and third contacts are concentric. Additionally or alternatively, at least two of the first, second and third contacts are concentric rings.

In accordance with a preferred embodiment of the present invention, the housing includes a top housing portion including key apertures, a plurality of data entry keys are mounted in the housing to have data entry key depression travel paths in the housing, the plurality of data entry keys being associated with a substrate, which is disposed below the top housing portion and anti-tampering electrical conductors are arranged between the top housing portion and the substrate thereby to prevent unauthorized access to the substrate.

e disclosures of which are incorporated by reference.

Reference is made to the following patent and patent applitions, owned by assignee, the disclosures of which are

Preferably, the substrate is a flexible substrate. Additionally ally or alternatively, the anti-tampering electrical conductors are disposed peripherally of the substrate.

In accordance with a preferred embodiment of the present invention, the data entry device also includes a plurality of key switch contacts mounted on the substrate and arranged to be displaced into or out of electrical contact with a corresponding plurality of electrical switches by depression of corresponding ones of the plurality of data entry keys to at least a predetermined extent along corresponding ones of the data entry key depression travel paths.

In accordance with a preferred embodiment of the present invention, the data entry device also includes a plurality of key switch contacts mounted on the plurality of data entry keys and arranged to be displaced into or out of electrical contact with a corresponding plurality of electrical switches
 by depression of corresponding ones of the plurality of data entry keys to at least a predetermined extent along corresponding ones of the data entry key depression travel paths.

Preferably, the plurality of data entry keys are integrally formed with the substrate.

In accordance with a preferred embodiment of the present invention, the anti-tampering electrical conductors are electrically connected to anti-tampering detection circuitry. Additionally or alternatively, the anti-tampering electrical conductors are disposed above and below the substrate. Additionally,
 the anti-tampering electrical conductors above the substrate and the anti-tampering electrical conductors below the substrate are electrically connected to anti-tampering detection circuitry. Additionally or alternatively, the anti-tampering electrical conductors above the substrate are electrically connected to the anti-tampering electrical conductors below the substrate.

In accordance with a preferred embodiment of the present invention, the housing includes a plurality of data entry keys, the data entry keys having data entry key depression travel paths in the housing, a plurality of key switch contacts are mounted on a flexible substrate separate from the plurality of data entry keys and arranged to be displaced into or out of electrical contact with a corresponding plurality of electrical switches by depression of corresponding ones of the plurality of data entry keys to at least a predetermined extent along corresponding ones of the data entry key depression travel paths and anti-tampering electrical conductors are arranged with respect to the plurality of key switch contacts thereby to prevent unauthorized electrical contact therewith.

Preferably, the anti-tampering electrical conductors are disposed above and below the flexible substrate. Additionally, the anti-tampering electrical conductors above the flexible

substrate and the anti-tampering electrical conductors below the flexible substrate are electrically connected to anti-tampering detection circuitry. Alternatively or additionally, the anti-tampering electrical conductors above the flexible substrate are electrically connected to the anti-tampering electri- 5 cal conductors below the flexible substrate.

In accordance with a preferred embodiment of the present invention, the plurality of data entry keys are integrally formed with the housing.

There is thus provided in accordance with a preferred 10 embodiment of the present invention a data entry device including a housing including a top housing portion including key apertures, a plurality of data entry keys mounted in the housing to have data entry key depression travel paths in the housing, the plurality of data entry keys being associated with a substrate, which is disposed below the top housing portion; and anti-tampering electrical conductors arranged between the top housing portion and the substrate thereby to prevent unauthorized access to the substrate.

Preferably, the substrate is a flexible substrate. In accor- 20 dance with a preferred embodiment of the present invention the anti-tampering electrical conductors are disposed peripherally of the substrate.

In accordance with a preferred embodiment of the present invention the data entry device also includes a plurality of key 25 switch contacts mounted on the substrate and arranged to be displaced into or out of electrical contact with a corresponding plurality of electrical switches by depression of corresponding ones of the plurality of data entry keys to at least a predetermined extent along corresponding ones of the data 30 entry key depression travel paths. Alternatively, the data entry device includes a plurality of key switch contacts mounted on the data entry keys and arranged to be displaced into or out of electrical contact with a corresponding plurality of electrical switches by depression of corresponding ones of the plurality 35 the assembled secure keypad device of FIGS. 1A and 1B; of data entry keys to at least a predetermined extent along corresponding ones of the data entry key depression travel

Preferably, the plurality of data entry keys are integrally formed with the substrate. In accordance with a preferred 40 embodiment of the present invention the anti-tampering electrical conductors are electrically connected to anti-tampering detection circuitry.

Preferably, the anti-tampering electrical conductors are disposed above and below the substrate. Additionally, the 45 anti-tampering electrical conductors above the substrate and the anti-tampering electrical conductors below the substrate are electrically connected to anti-tampering detection circuitry. Additionally or alternatively, the anti-tampering electrical conductors above the substrate are electrically con- 50 nected to the anti-tampering electrical conductors below the substrate.

There is also provided in accordance with another preferred embodiment of the present invention a data entry device including a housing element, including a plurality of 55 data entry keys disposed therein, the data entry keys having data entry key depression travel paths in the housing element, a plurality of key switch contacts mounted on a flexible substrate separate from the plurality of data entry keys and arranged to be displaced into or out of electrical contact with 60 a corresponding plurality of electrical switches by depression of corresponding ones of the plurality of data entry keys to at least a predetermined extent along corresponding ones of the data entry key depression travel paths and anti-tampering electrical conductors arranged with respect to the plurality of 65 key switch contacts thereby to prevent unauthorized electrical contact therewith.

Preferably, the anti-tampering electrical conductors are disposed above and below the flexible substrate. Additionally, the anti-tampering electrical conductors above the flexible substrate and the anti-tampering electrical conductors below the flexible substrate are electrically connected to anti-tampering detection circuitry. Alternatively or additionally, the anti-tampering electrical conductors above the substrate are electrically connected to the anti-tampering electrical conductors below the substrate.

In accordance with a preferred embodiment of the present invention the plurality of data entry keys are integrally formed with the housing element.

There is further provided in accordance with yet another preferred embodiment of the present invention data entry device including a housing element, including a plurality of data entry keys disposed therein, a plurality of housing-open indicating switch contacts and anti-tampering electrical conductors disposed effectively surrounding the plurality of housing-open indicating switch contacts thereby to prevent unauthorized electrical contact therewith.

In accordance with a preferred embodiment of the present invention the anti-tampering electrical conductors are electrically connected to anti-tampering detection circuitry.

Preferably, the plurality of data entry keys are integrally formed with the housing element.

BRIEF DESCRIPTION OF THE DRAWINGS

FIGS. 1A and 1B are simplified exploded view illustrations, taken in respective opposite directions, of a secure keypad device constructed and operative in accordance with a preferred embodiment of the present invention;

FIG. 1C is a simplified pictorial and sectional illustration of

FIGS. 2A and 2B are simplified exploded view illustrations, taken in respective opposite directions, of a secure keypad device constructed and operative in accordance with another preferred embodiment of the present invention;

FIG. 2C is a simplified pictorial and sectional illustration of the assembled secure keypad device of FIGS. 2A and 2B;

FIG. 3 is a simplified illustration of electrical connections between antis tampering elements in the embodiments of FIGS. 1A-2C;

FIGS. 4A and 4B are simplified exploded view illustrations, taken in respective opposite directions, of a secure keypad device constructed and operative in accordance with a further preferred embodiment of the present invention;

FIG. 4C is a simplified illustration the secure keypad device of FIGS. 4A and 4B at various levels of disassembly;

FIG. 5 is a simplified illustration of electrical connections between anti-tampering elements in the embodiments of FIGS. 4A-4C;

FIGS. 6A and 6B are simplified exploded view illustrations, taken in respective opposite directions, of a secure keypad device constructed and operative in accordance with yet a further preferred embodiment of the present invention;

FIG. 7 is a simplified illustration of electrical connections between anti-tampering elements in the embodiments of FIGS. 6A and 6B;

FIG. 8 is a simplified exploded view illustration of a secure keypad device constructed and operative in accordance with a still further preferred embodiment of the present invention;

FIGS. 9A and 9B are simplified sectional illustrations of part of the device of FIG. 8, taken along lines IX-IX in FIG. 8, in respective key non-depressed and key depressed operative orientations;

FIG. 10 is a simplified exploded view illustration of a secure keypad device constructed and operative in accordance with a yet further preferred embodiment of the present invention:

FIGS. **11A** & **11B** are simplified sectional illustrations of ⁵ part of the device of FIG. **10**, taken along lines XI-XI in FIG. **10**, in respective key non-depressed and key depressed operative orientations;

FIGS. 12A and 12B are simplified exploded view illustrations, taken in respective opposite directions, of a secure keypad device constructed and operative in accordance with still another preferred embodiment of the present invention;

FIG. 13A is a simplified partially sectional, partially schematic illustration of a first embodiment of a case open switch in a closed operative orientation;

FIG. 13B is a simplified sectional illustration of the first embodiment of a case open switch of FIG. 13A in an open operative orientation; and

FIG. **14A** is a simplified partially sectional, partially sche- ₂₀ matic illustration of a second embodiment of a case open switch in a closed operative orientation; and

FIG. 14B is a simplified sectional illustration of the second embodiment of a case open switch of FIG. 14A in an open operative orientation.

DETAILED DESCRIPTION OF PREFERRED EMBODIMENTS

Reference is now made to FIGS. 1A, 1B and 1C, which 30 illustrate a secure keypad device constructed and operative in accordance with a preferred embodiment of the present invention.

As seen in FIGS. 1A, 1B and 1C, there is provided a secure keypad device 100 including a housing element 102 which, 35 together with a back panel (not shown), defines a keypad device housing. Housing element 102 includes, on a top surface 104 thereof, a display aperture 106, through which a display (not shown) may be viewed, and an array 108 of key apertures 110.

A key mat 112, preferably formed of a resilient plastic or rubber, defines a plurality of depressible keys 114, preferably integrally formed with mat 112, which partially extend through key apertures 110. A flexible resilient protective partial enclosure 116, which includes an anti-tampering grid 45 118, formed of a multiplicity of interconnected anti-tampering electrical conductors, on a top surface 120 thereof and on side surfaces 122 thereof, is provided. A flexible cable 124 is coupled to grid 118.

Disposed within protective partial enclosure 116, underlying top surface 120, is a key contact pad 130. Key contact pad 130 is preferably a resilient, generally planar, pad formed of flexible and resilient plastic or rubber, having an array 132 of raised resilient conductive domes 134, such as those commercially available from Snaptron, Inc. of Windsor, Colo., USA. 55

Disposed in predetermined spaced relationship with key contact pad 130 is an electrical circuit board 140, which functions, inter alia, as a key contact board, defining a plurality of pairs of adjacent electrical contact pads 142, each pair underlying a corresponding dome 134, preferably made of 60 carbon, metal or combination of carbon/metal. The arrangement of key contact pad 130 and of electrical circuit board 140 is such that depression of a key 114 by the finger of a user causes dome 134 to establish electrical contact with and between a corresponding pair of electrical contact pads 142 65 lying thereunder and in registration therewith. When key 114 is not depressed, no electrical contact exists between dome

6

134 and a pair of corresponding electrical contact pads 142 or between the adjacent pads of the pair.

Electrical circuit board 140 preferably includes an antitampering grid 144 formed of a multiplicity of interconnected anti-tampering electrical conductors. An electrical connector 146 is arranged for mating connection with flexible cable 124. The anti-tampering grids 118 and 144 are coupled to antitampering detection circuitry 148.

In accordance with a preferred embodiment of the present invention, case-open switches, which sense physical tampering and opening of the housing, are provided, preferably within partial enclosure 116. In the illustrated embodiment, the case-open switches are each embodied in a raised resilient dome 150, preferably formed of a conductor, mounted on key contact pad 130. The domes may be discrete units or arrays, such as those commercially available from Snaptron, Inc. of Windsor, Colo., USA. Domes 150 preferably extend outwardly from the surface of key contact pad 130 to a greater extent than do domes 134.

Disposed on electrical circuit board 140 underlying each of domes 150 are a corresponding number of arrays 152, each preferably comprising a plurality of mutually concentric conductive pads, such as rings, each array underlying a corresponding dome 150. The arrangement of key contact pad 130 and of electrical circuit board 140 is preferably such that as long as the housing is closed, domes 150 each are in electrical contact with some but not all of the plurality of mutually concentric conductive rings in each of arrays 152 underlying domes 150 and in registration therewith.

In accordance with a first embodiment of the invention, indicated in FIG. 1A as Option A, each array 152 comprises an outer ring 153, an intermediate ring 154 and an inner ring 155. In this embodiment, as long as the housing is closed and not tampered with, the dome 150 is in electrical contact with outer ring 153 and inner ring 155 but not with intermediate ring 154. Dome 150 is normally in electrical contact with outer ring 153. Preferably, dome 150 is soldered to outer ring 153. If tampering is attempted, electrical contact is made between intermediate ring 154 and at least one of dome 150, outer ring 153 and inner ring 155.

In accordance with a second embodiment of the invention, indicated in FIG. 1A as Option B, each array 152 comprises an outermost ring 156, a second to outermost ring 157, an outer intermediate ring 158, an inner intermediate ring 159 and an inner ring 160, in this embodiment, as long as the housing is closed and not tampered with, the dome 150 is in electrical contact with outer intermediate ring 158 and inner ring 160 but not with any of outermost ring 156, second to outermost ring 157 and inner intermediate ring 159. Dome 150 is normally in electrical contact with outer intermediate ring 158. Preferably, dome 150 is soldered to outer intermediate ring 158. If tampering is attempted, electrical contact is made between at least one of outermost ring 156, second to outermost ring 157 and inner intermediate ring 159 and at least one of dome 150, outer intermediate ring 158 and inner ring 160.

Reference is now made additionally to FIGS. 13A and 13B and to FIGS. 14A and 14B, which are simplified partially sectional and partially schematic illustrations of respective first and second embodiments of the case open switch in respective closed and open operative orientations, coupled to case tamper detection circuitry.

As seen in FIGS. 13A and 13B, electrical contact between each dome 150 and corresponding ring 155 is ensured by the provision of a pin 161, preferably integrally formed on housing element 102. When the housing is closed, pins 161 urge corresponding protrusions 162 in key mat 112 towards partial

enclosure 116 and urge partial enclosure 116 and thus corresponding domes 150 towards ring 155 of array 152. In an alternative embodiment, where partial enclosure 116 is not provided, when the housing is closed, pins 161 urge corresponding protrusions 162 in key mat 112 and thus corresponding domes 150 towards arrays 152.

As seen in FIG. 13A, outer ring 153 is grounded, intermediate ring 154 is coupled to a voltage VDD via a resistor R1 and inner ring 155 is coupled to a voltage VDD via a resistor R2. A voltage V2 may be measured to indicate whether the housing is open or closed, i.e. whether or not dome 150 is simultaneously in contact with both inner ring 155 and outer ring 153.

When this simultaneous contact exists, V2 is zero; otherwise V2 equals VDD. An attempt to tamper with the switch by 15 short circuiting inner ring 155 and outer ring 153 will also short circuit intermediate ring 154 and may be detected by measuring a voltage V1. During normal operation, where no tampering is detected, V1 is equal to VDD. An attempt to tamper with the case open switch causes voltage V1 to be 20 zero.

As seen in FIGS. 14A and 14B, electrical contact between each dome 150 and corresponding ring 160 is ensured by the provision of pin 161, preferably integrally formed on housing element 102. When the housing is closed, pins 161 urge 25 corresponding protrusions 162 in key mat 112 towards partial enclosure 116 and urge partial enclosure 116 and thus corresponding domes 150 into electrical contact with rings 160. In an alternative embodiment, where partial enclosure 116 is not provided, when the housing is closed, pins 161 urge corresponding protrusions 162 in key mat 112 and thus corresponding domes 150 towards rings 160.

When attempted tampering occurs and the housing is opened, no electrical contact exists between domes **150** and corresponding rings **155** (FIG. **13**B) or **160** (FIG. **14**B) and a 35 suitable alarm and/or disablement occurs.

As seen in FIG. 14A, inner ring 160 is coupled to a voltage VCC via a resistor R2, inner intermediate ring 159 is coupled to voltage VCC via resistor R1, and outer intermediate ring 158 is grounded. Outermost ring 156 is grounded and second 40 to outermost ring 157 is coupled to voltage VCC via resistor R1

A voltage at tamper detection input points TD2 may be measured to indicate whether the hosing is open or closed, i.e. whether or not dome **150** is simultaneously in contact with 45 both inner ring **160** and outer intermediate ring **158**. When this simultaneous contact exists, voltage measured at TD2 is zero; otherwise voltage measured at TD2 equals VCC. Thus, an attempt to remove of adequate pressure from the dome **150** and open the housing triggers a tamper response.

An attempt to tamper with the switch, for example by applying pressure to the snap dome, such as by drilling a hole in the dome or by injection of conductive glue, results in short circuiting inner ring 160 and outer intermediate ring 158 and inner intermediate ring 159 and may be detected by measuring a voltage at nTD3. During normal operation, where no tampering is detected, voltage measured at nTD3 is equal to VCC. An attempt to tamper with the case open switch causes voltage measured at nTD3 to be zero.

It is appreciated that the rings of array 152, both in Option 60 A and Option B and corresponding FIGS. 13A & 13B and FIGS. 14A & 14B, can be of any suitable shape, such as circular or elliptical shape and are preferably concentric.

Disposed below electrical circuit board 140, there is provided a peripheral protective grid element 163 which includes 65 an anti-tampering grid 164, preferably coupled to a flexible cable 165, which is in turn connected to a connector 166 on

8

electrical circuit board 140. Peripheral protective grid element 163 preferably is located interiorly of the side surfaces 122 of enclosure 116. Fixedly attached to peripheral protective grid element 163 and preferably disposed therebelow is a bottom protective grid element 168 which includes an antitampering grid 170, which is coupled via contacts 172 and via a connector 174 which extends through an aperture 176 formed in peripheral grid element 163 into electrical contact with contacts 178 on electrical circuit board 140. The antitampering grids 164 and 170 are coupled to anti tampering detection circuitry 148.

Reference is now made to FIGS. 2A, 2B and 2C, which illustrate a secure keypad device constructed and operative in accordance with another preferred embodiment of the present invention.

As seen in FIGS. 2A-2C, there is provided a secure keypad device 200 including a housing element 202 which, together with a back panel (not shown), defines a keypad device housing. Housing element 202 includes, on a top surface 204 thereof, a display aperture 206, through which a display (not shown) may be viewed, and an array 208 of key apertures 210.

A key mat 212, preferably formed of a resilient plastic or rubber, defines a plurality of depressible keys 214, preferably integrally formed with mat 212, which partially extend through key apertures 210. A flexible resilient protective partial enclosure 216, which includes an anti-tampering grid 218, formed of a multiplicity of interconnected anti-tampering electrical conductors, on a top surface 220 thereof and on side surfaces 222 thereof, is provided. A flexible cable 224 is coupled to grid 218.

Disposed within protective partial enclosure 216, underlying top surface 220, is a key contact pad 230. Key contact pad 230 is preferably a resilient, generally planar, pad formed of flexible and resilient plastic or rubber, having an array 232 of raised resilient domes 234 having conductors 236 formed on corresponding bottom facing surfaces 238 thereof.

Disposed in predetermined spaced relationship with key contact pad 230 is a electrical circuit board 240, which functions, inter alia, as a key contact hoard, defining a plurality of pairs of adjacent electrical contact pads 242, each pair underlying a corresponding conductor 236. The arrangement of key contact pad 230 and of electrical circuit board 240 is such that depression of a key 214 by the finger of a user causes conductor 236 to establish electrical contact with and between a corresponding pair of electrical contact pads 242 lying thereunder and in registration therewith. When key 214 is not depressed, no electrical contact exists between conductor 236 and a pair of corresponding electrical contact pads 242 or between the adjacent pads of the pair.

Electrical circuit board 240 preferably includes an antitampering grid 244 formed of a multiplicity of interconnected anti-tampering electrical conductors. An electrical connector 246 is arranged for mating connection with flexible cable 224. The anti-tampering grids 218 and 214 are coupled to antitampering, detection circuitry 248.

In accordance with a preferred embodiment of the present invention, case-open switches, which sense physical tampering and opening of the housing, are also located within partial enclosure 216. In the illustrated embodiment the case-open switches are each embodied in a raised resilient dome 250 formed on key contact pad 230 and have conductors 252 formed on an underside surface thereof. Domes 250 preferably extend outwardly from the surface of key contact pad 230 to a greater extent than do domes 234. Alternatively, the case-open switches may be constructed and operative as described hereinabove with reference to one of the embodiments shown FIGS. 1A, 1B, 13A-14B.

Disposed on electrical circuit board 240 underlying each of domes 250 are a corresponding number of pairs of adjacent electrical contact pads 254, each pair underlying a corresponding conductor 252. The arrangement of key contact pad 230 and of electrical circuit board 240 is such that as long as 5 the housing is closed, conductors 252 each are in electrical contact with and between a corresponding pair of electrical contact pads 254 lying thereunder and in registration therewith. This electrical contact is ensured by the provision of pins 256 integrally formed on housing element 202. When the 10 housing is closed, pins 256 urge corresponding protrusions 258 in key mat 212 towards partial enclosure 216 and urge partial enclosure 216 and thus corresponding domes 250 and the conductors 252, on the underside surfaces thereof, into electrical contact with corresponding electrical contact pads 15 254.

When the housing is opened, no electrical contact exists between conductor **252** and corresponding pairs of electrical contact pads **254** or between the adjacent pads of the pair of electrical contact pads **254** and a suitable alarm or disable- 20 ment occurs

Disposed below electrical circuit board 240, there is provided a peripheral protective grid element 260 which includes an anti-tampering grid 262, preferably coupled to a flexible cable 264, which is in turn connected to a connector 266 on 25 electrical circuit board 240. Peripheral protective grid element 260 preferably is located interiorly of the side surfaces 222 of enclosure 216. Fixedly attached to peripheral protective grid element 260, and preferably disposed therebelow, is a bottom protective grid element 268 which includes an antitampering grid 270, which is coupled via contacts 272 and via a connector 274, which extends through an aperture 276 formed in peripheral grid element 260, into electrical contact with contacts 278 on electrical circuit board 240. The antitampering grids 262 and 270 are coupled to anti-tampering 35 detection circuitry 248.

In the illustrated embodiment of FIGS. 2A, 2B and 2C, there is also provided a structural peripheral enclosure element 280 which is disposed interiorly of the sides 222 of the flexible resilient protective partial enclosure 216 and which 40 provides a peripheral seating surface 282 for electrical circuit board 240.

Reference is now made to FIG. 3, which is a generalized illustration of the electrical connections described hereinabove and includes reference numerals corresponding to both 45 of the above-described embodiments shown respectively in FIGS. 1A-1C and 2A-2C. It may be appreciated from FIG. 3 that all of the anti-tampering grids are connected to the anti-tampering circuitry which is located within the protective enclosure.

Reference is now made to FIGS. 4A and 4B, which are simplified exploded view illustrations, taken in respective opposite directions, of a secure keypad device constructed and operative in accordance with a further preferred embodiment of the present invention, to FIG. 4C, which is a simplified illustration the secure keypad device of FIGS. 4A and 4B at various stages of disassembly, and to FIG. 5, which is a simplified illustration of electrical connections between antitampering elements in the embodiments of FIGS. 4A-4C. FIG. 4C is provided to better illustrate the various elements of 60 the keypad device of FIGS. 4A and 4B.

As seen in FIGS. 4A-5, there is provided a secure keypad device 400 including a housing element 402 which, together with a back panel (not shown), defines a keypad device housing. Housing element 402 includes, on a top surface 404 65 thereof, a display aperture 406, through which a display (not shown) may be viewed, and an array 408 of key apertures 410.

10

An anti-tamper board 412, which includes an anti-tampering grid 414 formed of a multiplicity of interconnected anti-tampering, electrical conductors 416, underlies top surface 404 and is provided with key apertures 420 in registration with key apertures 410. Fixedly and electrically coupled to anti-tamper board 412 is a peripheral anti-tamper keypad enclosure 422, which preferably includes an anti-tampering grid 423 formed of a multiplicity of interconnected anti-tampering electrical conductors.

A key mat 432, preferably formed of a resilient plastic or rubber, defines a plurality of depressible keys 434, preferably integrally formed with mat 432, which partially extend through key apertures 410 and 420 and have conductors 436 formed on corresponding bottom facing surfaces 438 thereof.

An electrical circuit board 440, which functions, inter alia, as a key contact board, is disposed in predetermined spaced relationship with key mat 432 and defines a plurality of pairs of adjacent electrical contact pads 442, each pair underlying a corresponding conductor 436. The arrangement of key mat 432 and of electrical circuit board 440 is such that depression of a key 434 by the finger of a user causes conductor 436 to establish electrical contact with and between a corresponding pair of electrical contact pads 442 lying thereunder and in registration therewith. When key 434 is not depressed, no electrical contact exists between conductor 436 and a pair of corresponding electrical contact pads 442 or between the adjacent pads of the pair.

Circuit board 440 preferably includes an anti-tampering grid 444 thrilled of a multiplicity of interconnected anti-tampering electrical conductors. Fixedly coupled to circuit board 440 and an additional electrical circuit board 456 is a peripheral antis tamper enclosure 460 which preferably includes an anti-tampering grid 461 formed of a multiplicity of interconnected anti-tampering electrical conductors.

Electrical connectors 446, which extend through apertures 443 formed in peripheral anti-tamper enclosure 460, are coupled via contacts 445 to grid 444 and arranged for mating connection through contacts 457 with an anti-tampering grid 454, formed of a multiplicity of interconnected anti-tampering, electrical conductors, which is formed on additional electrical circuit board 456.

Electrical connectors 471 extend through apertures 473 formed in peripheral anti-tamper keypad enclosure 422 and through apertures 475 formed in circuit board 440, each underlying corresponding aperture 473. Electrical connectors 471 are coupled via contacts 477 to anti-tampering grid 461 and arranged for mating connection through contacts 479 with anti-tampering grid 414.

Electrical connectors **481**, which extend through apertures **483** formed in keymat **432**, are coupled via contacts **485** to anti-tampering grid **414** and arranged for mating connection through contacts **487** with anti-tampering grid **444**.

Anti-tampering grid 423 is soldered via soldering points 489 on board 422 and soldering points 491 on board 412 to anti-tampering grid 414.

It is thus appreciated that the anti tampering grids 444, 414, 454, 423 and 461 on boards 440, 412 and 456 and enclosures 422 and 460, respectively, are interconnected so as to define an anti-tampering enclosure, which is coupled to and physically encloses anti-tampering detection circuitry 493.

It is appreciated that the anti-tampering grids can be interconnected in numerous ways using various types of connectors.

In accordance with a preferred embodiment of the present invention, case-open switches, which sense physical tampering and opening of the housing, are also located within the anti-tampering enclosure. In the illustrated embodiment, the

case-open switches are each embodied in conductors 462 mounted on key mat 432 and corresponding pair of electrical contact pads 464 mounted on board 440. Alternatively, the case-open switches may be constructed and operative as described hereinabove with reference to one of the embodiments shown FIGS. 1A, 1B, 13A-14B.

When the housing is closed, pins 465, which extend through apertures 466 in board 412, force conductors 462 and corresponding pair of electrical contact pads 464 into electrical contact or between the adjacent pads of the pair. When the 10 housing is opened, pins 465 no longer force conductors 462 and corresponding pair of electrical contact pads 464 into electrical contact and as a result electrical contact no longer exists between conductors 462 and corresponding pair of electrical contact pads 464 or between the adjacent pads of the 15 pair and a suitable alarm or disablement occurs.

Reference is now made FIGS. **6**A and **6**B, which are simplified exploded view illustrations, taken in respective opposite directions, of a secure keypad device constructed and operative in accordance with an additional preferred embodiment of the present invention, and to FIG. **7**, which is a simplified illustration of electrical connections between antitampering elements in the embodiments of FIGS. **6**A and **6**B.

As seen in FIGS. 6A-7, there is provided a secure keypad device 600 including a housing element 602 which, together 25 with a back panel (not shown), defines a keypad device housing. Housing element 602 includes, on a top surface 604 thereof, a display aperture 606, through which a display (not shown) may be viewed, and an array 608 of key apertures 610.

An anti-tamper board 612, which includes an anti-tampering grid 614 formed of a multiplicity of interconnected anti-tampering electrical conductors 616, underlies top surface 604 and is provided with key apertures 620 in registration with key apertures 610. Fixedly and electrically coupled to anti-tamper board 612 is a peripheral anti-tamper keypad 35 enclosure 622, which preferably includes an anti-tampering grid 624 formed of a multiplicity of interconnected anti-tampering electrical conductors.

A key mat 632, preferably formed of a resilient plastic or rubber, defines a plurality of depressible keys 634 which 40 partially extend through key apertures 610 and 620. A flexible resilient protective element 636, preferably a resilient generally planar pad formed of flexible and resilient plastic or rubber which includes an anti-tampering grid 638 formed of a multiplicity of interconnected anti-tampering electrical 45 conductors, is provided. An electrical cable 640 is coupled to grid 638.

Disposed below element **636** is a key contact pad **642**, preferably a resilient generally planar pad formed of flexible and resilient plastic or rubber, having an array **644** of raised 50 resilient domes **646** having conductors **648** formed on corresponding bottom facing surfaces **650** thereof.

An electrical circuit board 660 is disposed in predetermined spaced relationship with key mat 632 and defines a plurality of pairs of adjacent electrical contact pads 662, each 55 pair underlying a corresponding conductor 648. The arrangement of key mat 632 and of electrical circuit board 660 is such that depression of a key 634 by the finger of a user causes conductor 648 to establish electrical contact with and between a corresponding pair of electrical contact pads 662 lying thereunder and in registration therewith. When key 634 is not depressed, no electrical contact exists between conductor 648 and a pair of corresponding pads 662 or between the adjacent pads of the pair.

Circuit board **660** preferably includes an anti-tampering 65 grid **664**, formed of a multiplicity of interconnected anti-tampering electrical conductors. Fixedly and electrically

12

coupled to board 660 and to an additional electrical circuit board 670 is a peripheral anti-tamper enclosure 672, which preferably includes an anti-tampering grid 676 formed of a multiplicity of interconnected anti-tampering electrical conductors.

Electrical connectors 666, which extend through apertures 667 in peripheral anti-tamper enclosure 672, are coupled via contacts 665 to grid 664 and arranged for mating connection through contacts 669 with an anti-tampering grid 668 formed of a multiplicity of interconnected anti-tampering electrical conductors on additional electrical circuit board 670.

Electrical connectors 677 extend through apertures 679 formed in peripheral anti-tamper keypad enclosure 622 and through apertures 681 formed in board 660, each underlying corresponding aperture 679. Electrical connectors 677 are coupled via contacts 673 to anti-tampering grid 676 and arranged for mating connection through contacts 675 with anti-tampering grid 614.

Flexible electrical cable **640** is connected to a connector **683** on electrical circuit board **660**.

Electrical connectors 691 extend through apertures 693 formed in flexible element 636 and through apertures 695 formed in key contact pad 642 and corresponding apertures 696 formed in key mat 632. Electrical connectors 691 are coupled via contacts 697 to anti-tampering grid 664 and arranged for mating connection through contacts 699 with an anti-tampering grid 614.

Anti-tampering grid 624 is soldered to anti-tampering grid 614.

It is thus appreciated that the anti tampering grids 614, 664, 668, 638, 624 and 676, on boards 612, 660 and 670, element 636 and enclosures 622 and 672, respectively, are interconnected so as to define an anti-tampering enclosure, which is coupled to and physically encloses anti-tampering detection circuitry 674.

In accordance with a preferred embodiment of the present invention, case-open switches, which sense physical tampering and opening of the housing, are also located within the anti-tampering enclosure. In the illustrated embodiment the case-open switches are each embodied in conductors 680 mounted on the bottom facing of domes 641 of key contact pad 642 and corresponding pairs of electrical contact pads 682 mounted on board 660. Alternatively, the case-open switches may be constructed and operative as described hereinabove with reference to one of the embodiments shown FIGS. 1A, 1B, 13A-14B.

When the housing is closed, pins 684, which extend through apertures 686 in board 612, urge corresponding protrusions 631 on key mat 632 towards element 636 and key contact pad 642 and urge domes 641 on key contact pad 642 towards board 660 and thus force conductors 680 and pair of electrical contact pads 682 into electrical contact. When the housing is opened, pins 684 no longer force conductors 680 and pair of electrical contact pads 682 into electrical contact and as a result electrical contact no longer exists between conductors 680 and pair of electrical contact pads 682 and a suitable alarm or disablement occurs.

Reference is now made to FIG. **8**, which is a simplified exploded view illustration of a secure keypad device constructed and operative in accordance with a still further preferred embodiment of the present invention, and to FIGS. **9**A & **9**B, which are simplified sectional illustrations of part of the device of FIG. **8**, taken along lines IX-IX in FIG. **8**, in respective key non-depressed and key depressed operative orientations.

As seen in FIGS. 8-9B, there is provided a secure keypad device 800 including a housing element 802 which, together

with a back panel (not shown), defines a keypad device housing. Housing element **802** includes, on a top surface **804** thereof, a display aperture **806**, through which a display (not shown) may be viewed, and an array **808** of key apertures **810**.

An anti-tamper board **812**, which includes an anti-tampering grid **814** formed of a multiplicity of interconnected antitampering electrical conductors, underlies top surface **804** and is provided with key apertures **820** in registration with key apertures **810**. Fixedly and electrically coupled to antitamper hoard **812** is a peripheral anti-tamper keypad enclosure **822**, which preferably includes an anti-tampering grid **824** formed of a multiplicity of interconnected anti-tampering electrical conductors.

A key mat **832**, preferably formed of a resilient plastic or rubber, defines a plurality of depressible keys **834** which 15 partially extend through key apertures **810** and **820**. Conductors **836** are formed on key mat **832**.

Pairs of adjacent electrical contact pads 838 are located on board 812 in registration with conductors 836 and are arranged such that when keys 834 are in a non-depressed 20 operative orientation, such as that shown in FIG. 9A, electrical contact exists between conductors 836 and pairs of adjacent electrical contact pads 838 lying thereover and in registration therewith and between those pairs of adjacent electrical contact pads 838. When keys 834 are in a depressed 25 operative orientation, such as that shown in FIG. 9B, no electrical contact exists between conductors 836 and pairs of adjacent electrical contact pads 838 lying thereover and in registration therewith and between those pairs of adjacent electrical contact pads 838.

An electrical circuit board 840 is disposed in predetermined spaced relationship with key mat 832. Board 840 preferably includes an anti-tampering grid 844 formed of a multiplicity of interconnected anti-tampering electrical conductors. Electrical connectors 846 are coupled to grid 844 35 and arranged for mating connection with an anti-tampering grid 854, formed of a multiplicity of interconnected antitampering electrical conductors, which is formed on an additional electrical circuit board 856. Fixedly and electrically coupled to boards 840 and 856 is a peripheral anti-tamper 40 enclosure 858 which preferably includes an anti-tampering grid 859, formed of a multiplicity of interconnected antitampering electrical conductors. It is thus appreciated that the anti tampering grids 844, 854 and 859, on boards 840 and 856 and peripheral anti-tamper enclosure 858, respectively, are 45 interconnected so as to define an anti-tampering enclosure, which is coupled to and physically encloses anti-tampering detection circuitry (not shown). It is also appreciated that anti tampering grids 814 and 824 on board 812 and enclosure 822 are coupled to anti-tampering circuitry (not shown) and pref- 50 erably interconnected to anti tampering grids 844, 854 and **859** on boards **840** and **856** and enclosure **858**.

In accordance with a preferred embodiment of the present invention, case-open switches, which sense physical tampering and opening of the housing, are also located within the 55 anti-tampering enclosure. In the illustrated embodiment, the case-open switches are each embodied in conductors (not shown) mounted on key mat 832 and formed on underside surface thereof, and corresponding pair of electrical pads 864 mounted on board 840. Alternatively, the case-open switches 60 may be constructed and operative as described hereinabove with reference to one of the embodiments shown FIGS. 1A, 1B, 13A-14B.

When the housing is closed, pins (not shown) extend through apertures (not shown) in board 812 and force pair of electrical contacts 864 and corresponding conductors (not shown) on key mat 832 into electrical contact. When the

14

housing is opened, pins 864 no longer force conductors on key mat 832 and pair of electrical pads 864 into electrical contact and as a result electrical contact no longer exists between conductors on key mat 832 and pair of electrical contacts 864 and a suitable alarm or disablement occurs.

Reference is now made to FIG. 10, which is a simplified exploded view illustration of a secure keypad device constructed and operative in accordance with a yet further preferred embodiment of the present invention, and to FIGS. 11A & 11B, which are simplified sectional illustrations of part of the device of FIG. 10, taken along lines XI-XI in FIG. 10, in respective key non-depressed and key depressed operative orientations.

As seen in FIGS. 10-11B, there is provided a secure keypad device 900 including a housing element 902 which, together with a back panel (not shown), defines a keypad device housing. Housing element 902 includes a display aperture 906, through which a display (not shown) may be viewed, and an array 908 of key apertures 910.

An anti-tamper board 912, which includes an anti-tampering grid 914 formed of a multiplicity of interconnected anti-tampering electrical conductors, underlies top surface of housing element 902 and is provided with key apertures 920 in registration with key apertures 910. Fixedly and electrically coupled to anti-tamper board 912 is a peripheral anti-tamper keypad enclosure 922, which preferably includes an anti-tampering grid 923, formed of a multiplicity of interconnected anti-tampering electrical conductors.

A key mat 932, preferably formed of a resilient plastic or rubber, defines a plurality of depressible keys 934 which partially extend through key apertures 910 and 920. Conductors 936 are formed on an underside surface 938 of key mat 932, preferably on opposite sides of each of keys 934.

An electrical circuit board 940, which functions, inter alia, as a key contact board, is disposed in predetermined spaced relationship with key mat 932 and defines a plurality of pairs of adjacent electrical contact pads 942, each pair underlying a corresponding conductor 936.

When keys 934 are in a non-depressed operative orientation, such as that shown in FIG. 11A, electrical contact does not exist between conductors 936 and pairs of adjacent electrical contact pads 942 lying thereunder and in registration therewith and between those pairs of adjacent electrical contact pads 942. When keys 934 are in a depressed operative orientation, such as that shown in FIG. 11B, electrical contact exists between conductors 936 and pairs of adjacent electrical contact pads 942 lying thereunder in registration therewith and between those pairs of adjacent electrical contact pads 942.

Board 940 preferably includes an anti-tampering grid 944, formed of a multiplicity of interconnected anti-tampering electrical conductors. Electrical connectors 946 are coupled to grid 944 and arranged for mating connection with an antitampering grid 954, formed of a multiplicity of interconnected anti-tampering electrical conductors, which is formed on an additional electrical circuit board 956. Fixedly and electrically coupled to boards 940 and 956 is a peripheral anti-tamper enclosure 958 which preferably includes an antitampering grid 957 formed of a multiplicity of interconnected anti-tampering electrical conductors. It is appreciated that the anti tampering grids 944, 954 and 957 on boards 940 and 956 and peripheral anti-tamper enclosure 958, respectively, are interconnected so as to define an anti-tampering enclosure, which is coupled to and physically encloses anti-tampering detection circuitry 959. Electrical connectors 980 and 982

preferably couple grids 914 and 923 on board 912 and enclosure 922, respectively, to anti-tampering detection circuitry 959

In accordance with a preferred embodiment of the present invention, case-open switches, which sense physical tampering and opening of the housing are also located within the anti-tampering enclosure. In the illustrated embodiment the case-open switches are each embodied in conductors 962 mounted on key mat 932 and corresponding conductors (not shown) mounted on board 940. Alternatively, the case-open switches may be constructed and operative as described hereinabove with reference to one of the embodiments shown FIGS. 1A, 1B, 13A-14B.

When the housing is closed, pins 966 extend through apertures 968 in board 912 and force conductors 962 into electrical contact with the corresponding conductors on board 940. When the housing is opened, pins 966 no longer force conductors 962 into electrical contact with the corresponding conductors on board 940 and as a result electrical contact no longer exists between conductors 962 and the corresponding conductors on board 940 and a suitable alarm or disablement occurs

Reference is now made to FIGS. 12A and 12B, which are simplified exploded view illustrations, taken in respective opposite directions, of a secure keypad device constructed ²⁵ and operative in accordance with still another preferred embodiment of the present invention.

As seen in FIGS. 12A and 12B, there is provided a secure keypad device 1100 including a housing element 1102 which, together with a hack panel (not shown), defines a keypad 30 device housing. Housing element 1102 includes, on a top surface 1104 thereof; a display aperture 1106, through which a display (not shown) may be viewed, and an array 1108 of depressible keys 1110, preferably integrally formed with housing element 1102.

Keys 1110 are preferably formed of a resilient plastic or rubber and partially extend above top surface 1104 of housing element 1102. As seen particularly in section A-A enlargement in FIG. 12A, a contact surface 1112 of each of keys 1110 extends below top surface 1104 of housing element 1102. Keys 1110 preferably include an attachment portion 1113 and a connection portion 1114.

A flexible resilient protective partial enclosure 1116, which includes an anti-tampering grid 1118, formed of a multiplicity of interconnected anti-tampering electrical conductors, on a top surface 1120 thereof and on side surfaces 45 1122 thereof, is provided. A flexible cable 1124 is coupled to grid 1118.

Disposed within protective partial enclosure 1116, underlying top surface 1120, is a key contact pad 1130. Key contact pad 1130 is preferably a resilient, generally planar, pad 50 thrilled of flexible and resilient plastic or rubber, having an array 1132 of raised resilient domes 1134 with conductors 1136 being formed on corresponding bottom facing surfaces 1138 thereof.

Disposed in predetermined spaced relationship with key contact pad 1130 is an electrical circuit board 1140, which functions, inter alia, as a key contact board, defining a plurality of pairs of adjacent electrical contact pads 1142, each pair underlying a corresponding conductor 1136, preferably made of carbon, metal or combination of carbon/metal. The arrangement of key contact pad 1130 and of electrical circuit board 1140 is such that depression of a key 1110 by the finger of a user causes conductor 1136 to establish electrical contact with and between a corresponding pair of electrical contact pads 1142 lying thereunder and in registration therewith. When key 1110 is not depressed, no electrical contact exists between conductor 1136 and a pair of corresponding electrical contact pads 1142 or between the adjacent pads of the pair.

16

Electrical circuit hoard 1140 preferably includes an antitampering grid 1144 formed of a multiplicity of interconnected anti-tampering electrical conductors. An electrical connector 1146 is arranged for mating connection with flexible cable 1124. The anti-tampering grids 1118 and 1144 are coupled to anti-tampering detection circuitry 1148.

In accordance with a preferred embodiment of the present invention, case-open switches, which sense physical tampering and opening of the housing, are also located within partial enclosure 1116. In the illustrated embodiment, the case-open switches are each embodied in a raised resilient dome 1150 formed on key contact pad 1130 and have conductors 1152 formed on an underside surface thereof. Domes 1150 preferably extend outwardly from the surface of key contact pad 1130 to a greater extent than do domes 1134. Alternatively, the case-open switches may be constructed and operative as described hereinabove with reference to one of the embodiments shown FIGS. 1A, 1B, 13A-14B.

Disposed on electrical circuit board 1140 underlying each of domes 1150 are a corresponding number of pairs of adjacent electrical contact pads 1154, each pair underlying a corresponding conductor 1152. The arrangement of key contact pad 1130 and of electrical circuit board 1140 is such that as long as the housing is closed, conductors 1152 each are in electrical contact with and between a corresponding pair of electrical contact pads 1154 lying thereunder and in registration therewith. This electrical contact is ensured by the provision of pins 1156 integrally formed on housing element 1102. When the housing is closed, pins 1156 urge element 1116 and thus corresponding domes 1150 on contact pad 1130, and thus corresponding conductors 1152, on the underside surfaces thereof, into electrical contact with corresponding electrical contact pads 1154.

When the housing is opened, no electrical contact exists between conductor 1152 and corresponding pairs of electrical contact pads 1154 or between the adjacent pads of the pair of electrical contact pads 1154 and a suitable alarm or disablement occurs.

Disposed below electrical circuit board 1140, there is provided a peripheral protective grid element 1160 which includes an anti-tampering grid 1162, preferably coupled to a flexible cable 1164, which is in turn connected to a connector 1166 on electrical circuit board 1140. Peripheral protective grid element 1160 preferably is located interiorly of the side surfaces 1122 of enclosure 1116. Fixedly attached to peripheral protective grid element 1160 and preferably disposed therebelow is a bottom protective grid element 1168 which includes an anti-tampering grid 1170, which is coupled via contacts 1172 and via a connector 1174 which extends through an aperture 1176 formed in peripheral grid element 1160 into electrical contact with contacts 1178 on electrical circuit board 1140. The anti-tampering grids 1162 and 1170 are coupled to anti-tampering detection circuitry 1148.

It is appreciated by persons skilled in the art that the present invention is not limited by what has been particularly shown and described hereinabove. Rather the scope of the present invention includes both combinations and subcombinations of various features described hereinabove as well as variations and modifications thereto which would occur to a person of skill in the art upon reading the above description and which are not in the prior art.

The invention claimed is:

- 1. A device comprising:
- an enclosure;
- circuitry located on an electrical circuit board within said enclosure;
- at least one switch assembly operative to sense when said enclosure is opened; and
- tamper indication circuitry operative to receive an input from said at least one switch assembly and to provide an

output indication of possible tampering with said circuitry located on said electrical circuit board within said enclosure.

said at least one switch assembly including a raised resilient dome and an arrangement of electrical contacts located on said electrical circuit board, said electrical contacts including at least first, second and third mutually concentric conductive pads all underlying said dome; said at least first, second and third mutually concentric conductive pads and said raised resilient dome being configured and arranged such that as long as said enclosure is closed in a non-tampered state, said raised resilient dome is in electrical contact with some but not all of said at least first, second and third mutually concentric conductive pads,

said tamper indication circuitry being operative such that when electrical connections between said at least first, second and third mutually concentric conductive pads change from those in said non-tampered state, an output indication of possible tampering is provided.

- 2. A device according to claim 1 and wherein said at least first, second and third mutually concentric conductive pads are connected to said tamper indication circuitry.
 - 3. A data entry device according to claim 1 and wherein: said enclosure includes a top portion including key apertures:
 - a plurality of keys are mounted in said enclosure to have key depression travel paths in said enclosure, said plurality of keys being associated with a substrate, which is disposed below said top portion; and

anti-tampering electrical conductors are arranged between said top portion and said substrate thereby to prevent unauthorized access to said substrate.

- **4.** A device device according to claim **3** and wherein said substrate is a flexible substrate.
- 5. A device according to claim 3 and wherein said antitampering electrical conductors are disposed peripherally of said substrate.
- 6. A device according to claim 3 and also comprising a plurality of key switch contacts mounted on said substrate and arranged to be displaced into or out of electrical contact with

18

a corresponding plurality of electrical switches by depression of corresponding ones of said plurality of keys to at least a predetermined extent along corresponding ones of said key depression travel paths.

- 7. A device according to claim 3 and also comprising a plurality of key switch contacts mounted on said plurality of keys and arranged to be displaced into or out of electrical contact with a corresponding plurality of electrical switches by depression of corresponding ones of said plurality of keys to at least a predetermined extent along corresponding ones of said key depression travel paths.
- **8**. A device according to claim **3** and wherein said plurality of keys are integrally formed with said substrate.
- 9. A device according to claim 3 and wherein said antitampering electrical conductors are electrically connected to anti-tampering detection circuitry.
- 10. A device according to claim 3 and wherein said antitampering electrical conductors are disposed above and below said substrate.
- 11. A device according to claim 10 and wherein said antitampering electrical conductors above said substrate and said anti-tampering electrical conductors below said substrate are electrically connected to anti-tampering detection circuitry.
- 12. A device according to claim 10 and wherein said antitampering electrical conductors above said substrate are electrically connected to said anti-tampering electrical conductors below said substrate.
- 13. A device according to claim 1 and wherein said raised resilient dome is in a first operative orientation vis-à-vis said at least first, second and third mutually concentric conductive pads when said enclosure is dosed and is in a second operative orientation vis-à-vis said at least first, second and third mutually concentric conductive pads when said enclosure is open.
- 14. A device according to claim 13 and also comprising at least one urging element which normally urges said raised resilient dome into said first operative orientation vis-à-vis said at least first, second and third mutually concentric conductive pads when said enclosure is closed.
- 15. A device according to claim 14 and also wherein said at 40 least one urging element is mounted on said enclosure.

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